

**AMENDMENTS TO THE CLAIMS**

Please replace the claims, including all prior versions, with the listing of claims found below.

**Listing of Claims:**

1. (currently amended) A transceiver, comprising:

an RF receiver with a digital signal processing device in a digital part with an input part;

at least one mixer; and

an intermediate frequency/baseband processing device having a local channel oscillator to which a first phase locked loop with a phase discriminator and an adjustable first frequency converter are assigned, and having a reference oscillator for the first phase locked loop and a control clock of the digital signal processing device,

wherein ~~in order~~ to acquire the control clock ( $f_{STE}$ ), a digital clock synthesizer is provided to which an upward signal of the reference oscillator is supplied, and a control signal is supplied by the digital part in the form of a digital tuning word, the frequency of the reference oscillator being selected such that its order of magnitude is at least equal to the bandwidth of one or more of the reception bands used and harmonics do not occur in a reception channel.

2. (Previously presented) The transceiver as claimed in claim 1, wherein the clock synthesizer is a synthesizer with direct digital synthesis.

3. (Previously presented) The transceiver as claimed in claim 1, wherein the first frequency converter is a DDS synthesizer or a fractional N divider with a sigma-delta modulator.

4. (Previously presented) The transceiver as claimed in claim 1, wherein a fine-tuning signal is supplied to the first frequency converter by the digital part.
5. (Previously presented) The transceiver as claimed in claim 1, further comprising an IF oscillator to which a second phase locked loop with a phase discriminator and a second frequency converter is assigned, an output signal of the reference oscillator being supplied to the phase discriminator, and a modulation signal and a fine-tuning signal being supplied to the frequency converter by the digital part.
6. (Currently amended) The transceiver as claimed in claim ~~[[1]]~~5, wherein the frequency converter assigned to the IF oscillator is a DDS synthesizer or a fractional N divider with sigma-delta modulator.
7. (Currently amended) The transceiver as claimed in ~~[[one claim 1]]~~claim 5, wherein the ~~modulation~~ reference oscillator signal is supplied to the ~~IF oscillator~~ first frequency converter from the digital part.
8. (Currently amended) The transceiver as claimed in claim ~~[[1]]~~5, wherein the modulated IF signal is generated using frequency conversion, frequency division or DDS.
9. (Currently amended) The transceiver as claimed in claim 1, wherein the transceiver ~~transceiver~~ is a homodyne receiver in which the modulated RF carrier, and a direct or rearranged output signal of

the local channel oscillator are supplied to a reception mixer so that the intermediate frequency corresponds to the baseband.

10. (Previously presented) The transceiver as claimed in claim 5, wherein the modulated output signal of the IF oscillator and the output signal of a transmission mixer are fed to a phase discriminator, to which a signal of a controlled transmission oscillator and an output signal of the local channel oscillator are supplied.